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2/8/07  
Patent No. : 6909661  
Inventor(s) : Yang et al.  
Issued : 6/21/05  
Title : SEMICONDUCTOR MEMORY DEVICE WITH DATA INPUT/OUTPUT  
ORGANIZATION IN MULTIPLES OF NINE BITS  
Atty.doc./File No.

Request for Certificates of Correction

Consideration has been given to your request for the issuance of a Certificate of Correction, for the above – identified patent under the provisions of CFR 1.322.

In reponse to telephone inquire to this Office, the decision denying your request dated January 22, 200[6]7, is hereby retracted.

A certificate of correction will be issued/published and attached to the patent image on the Internet. Also, you will receive a reprinted/corrected U.S. Patent (ribboned grant).

Any response to this letter or any inquiry concerning your request, should be directed to:

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450  
Attention: RoChaun Johnson  
Certificates of Correction Branch

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